

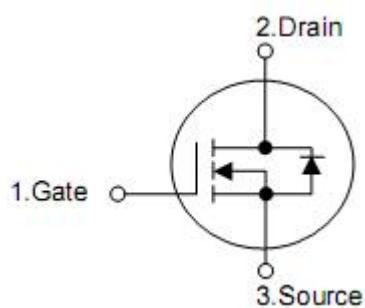
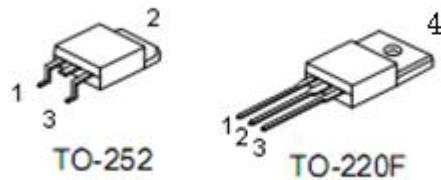
## 1. Features

- $R_{DS(ON),typ.}=2.0\Omega$  @  $V_{GS}=10V, I_D=2A$
- Fast Switching
- 100% avalanche tested
- Improved dv/dt capability

## 2. Application

- High frequency switching mode power supply
- Uninterruptible Power Supply(UPS)
- Electronic ballast

## 3. Pin configuration



Pin	Function
1	Gate
2	Drain
3	Source
4	Drain

## 4. Ordering Information

Part Number	Package	Brand
KND4365A	TO-252	KIA
KNF4365A	TO-220F	KIA

## 5. Absolute maximum ratings

(T <sub>C</sub> = 25°C , unless otherwise specified)				
Parameter	Symbol	Rating		Units
		TO252	TO220F	
Drain-source voltage	V <sub>DSS</sub>	650		V
Gate-source voltage	V <sub>GSS</sub>	±30		V
Continuous Drain Current T <sub>C</sub> =25 °C	I <sub>D</sub>	4*	4*	A
T <sub>C</sub> =100 °C		2.78*	2.78*	A
Pulsed Drain Current <sup>note1</sup>	I <sub>DM</sub>	16*	16*	A
Single Pulse Avalanche Energy <sup>note2</sup>	E <sub>AS</sub>	180		mJ
Peak Diode Recovery Energy <sup>note3</sup>	dv/dt	4.8		V/ns
Power Dissipation	T <sub>C</sub> =25 °C	55	44.6	W
Linear Derating Factor		0.46	0.357	W/°C
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	2.16	3.75	°C/W
Operating and Storage Temperature Range	T <sub>L,T<sub>STG</sub></sub>	-55~+150		°C

\*Drain current limited by maximum junction temperature



## 7. Typical Characteristics

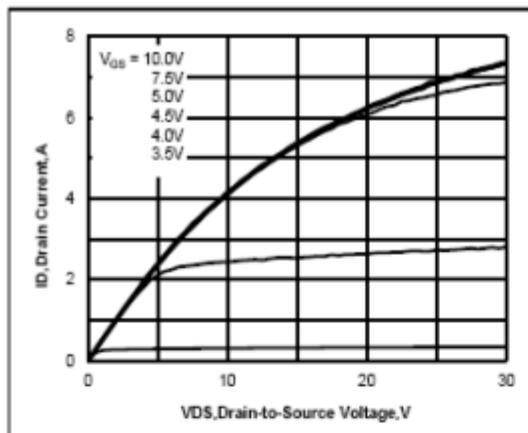


Figure 1. Output Characteristics

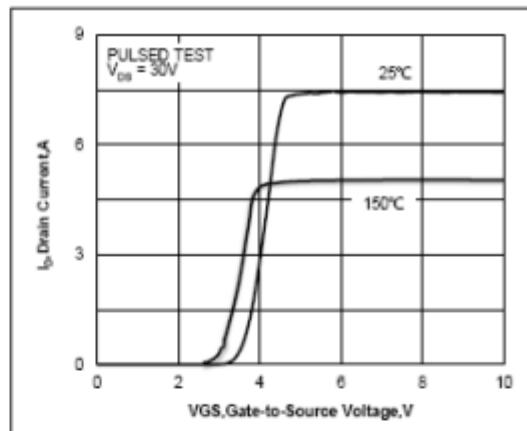


Figure 2. Transfer Characteristics

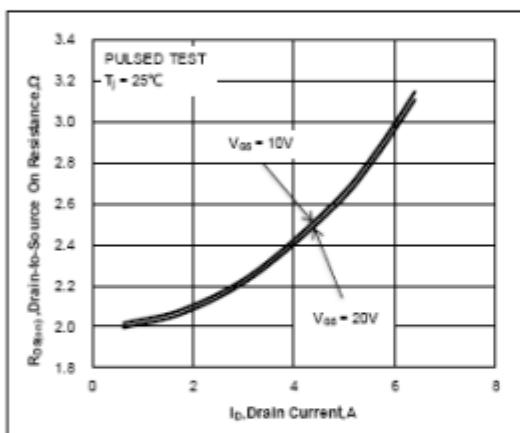


Figure 3. Drain-to-Source On Resistance vs.  
Drain Current and Gate Voltage

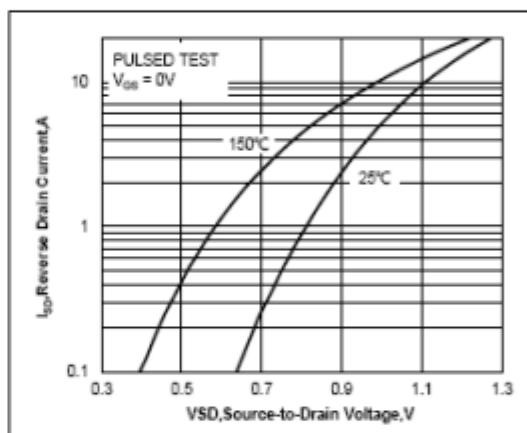


Figure 4. Body Diode Forward Voltage vs.  
Source Current and Temperature

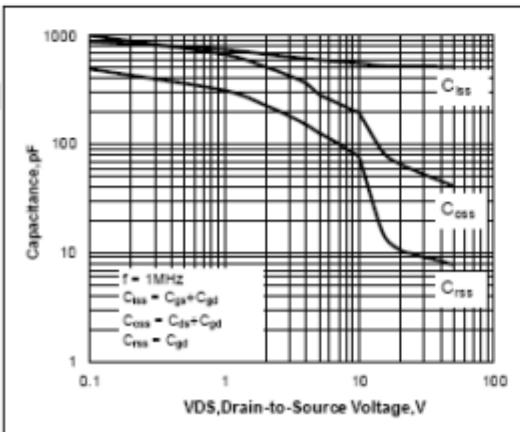


Figure 5. Capacitance Characteristics

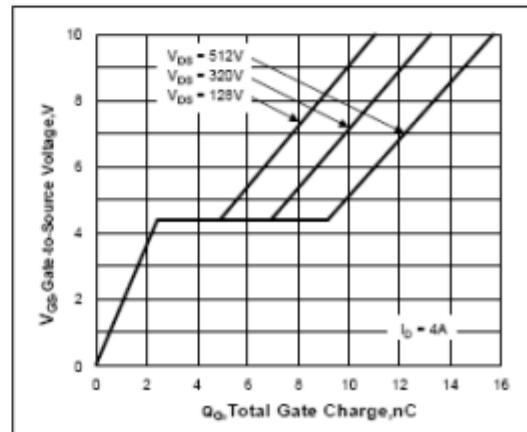
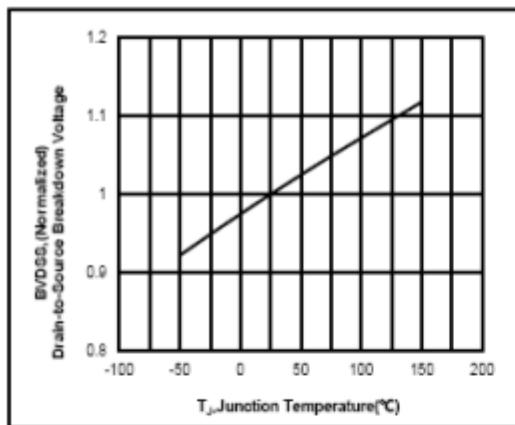
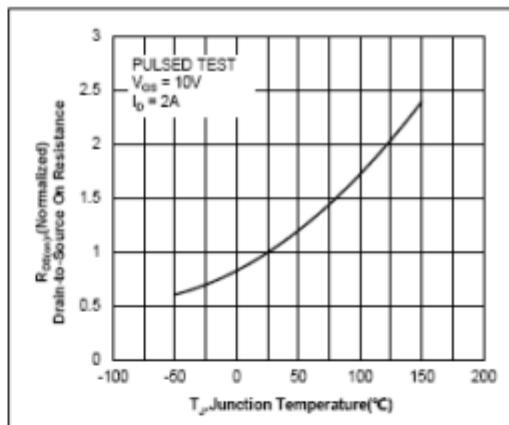


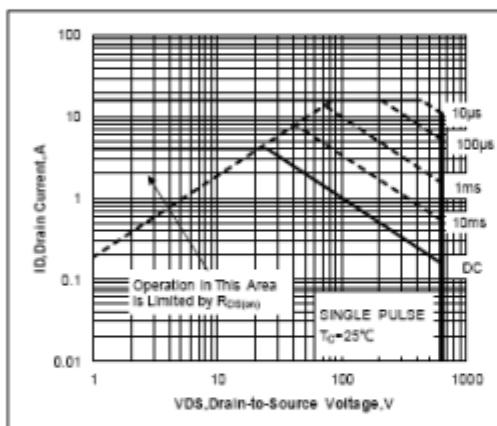
Figure 6. Gate Charge Characteristics



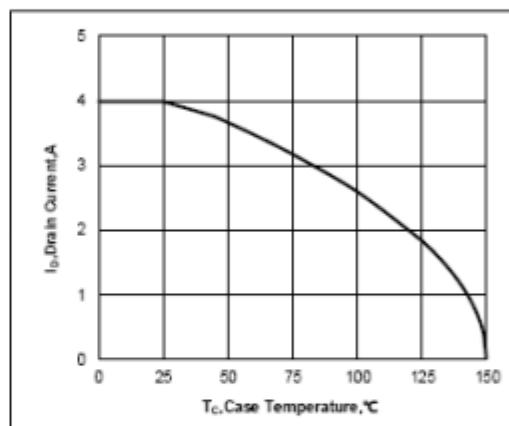
**Figure 7. Normalized Breakdown Voltage vs.  
Junction Temperature**



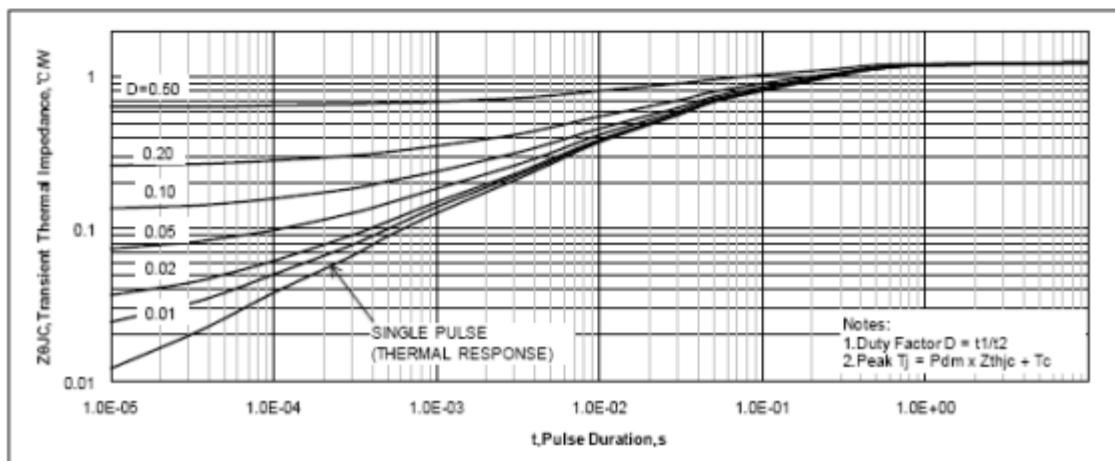
**Figure 8. Normalized On Resistance vs.  
Junction Temperature**



**Figure 9. Maximum Safe Operating Area**



**Figure 10. Maximum Continuous Drain Current vs.  
Case Temperature**



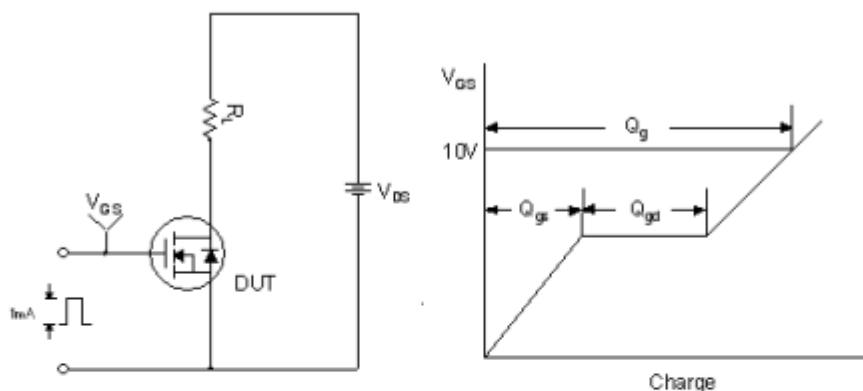


Figure 12. Gate Charge Test Circuit & Waveform

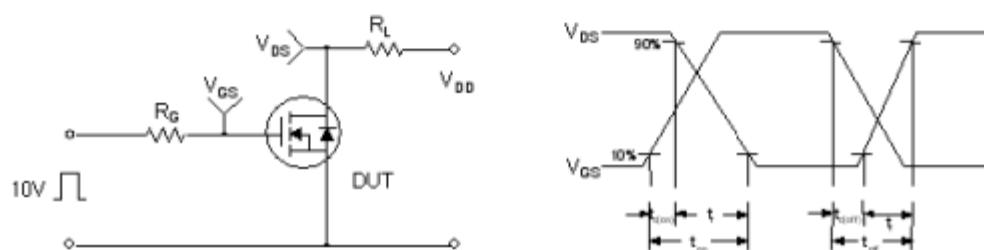


Figure 13. Resistive Switching Test Circuit & Waveforms

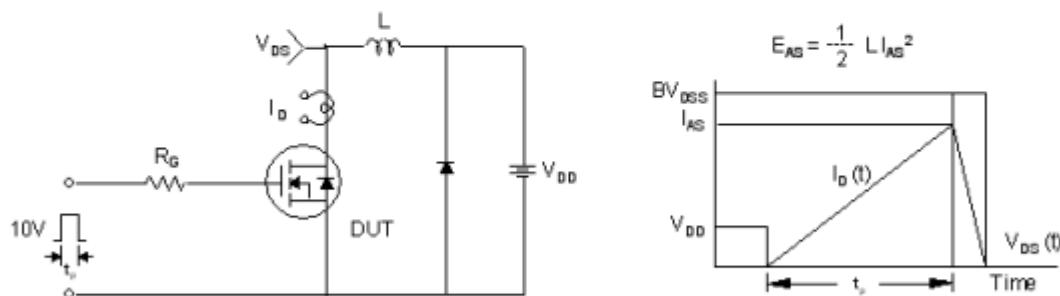
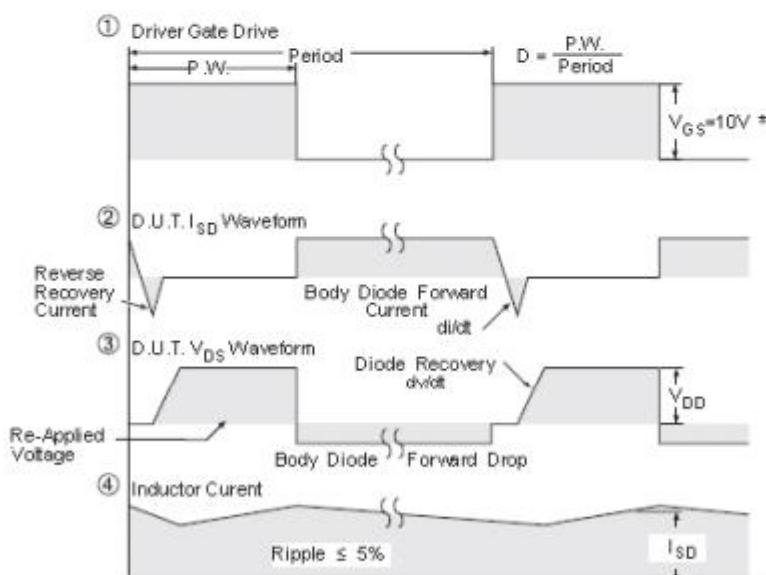
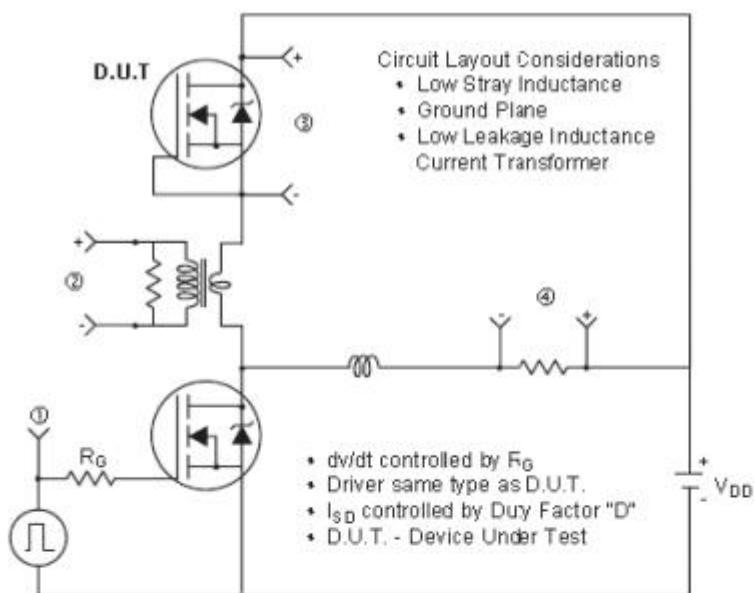


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



\*  $V_{GS} = 5V$  for Logic Level Devices

Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms (For N-channel)